



#11/IDS 2829
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Docket No.: GR 98 P 1916 P

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By: [Signature]

Date: July 10, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wilhelm Schmid et al.
Applic. No. : 09/740,634
Filed : December 18, 2000
Title : Device for Measurement and Analysis of Electrical Signals of an Integrated Circuit Component
Examiner : Paresh H. Patel - Art Unit: 2829

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(C)(2)

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,712,858 (Godiwala et al.), dated January 27, 1998;

European Patent Application EP 0 650 069 A2 (Dagostino et al.), dated April 26, 1995;

International Search Report dated November 11, 1999.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,



For Applicants

Mark P. Weichselbaum
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Date: July 10, 2002

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